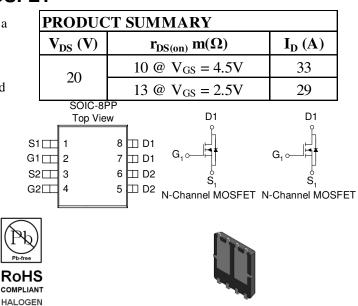
AM7920N

Analog Power

Dual N-Channel 20-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low $r_{DS(on)}$ and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low r_{DS(on)} provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe SOIC-8PP saves board space
- Fast switching speed
- High performance trench technology



ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C UNLESS OTHERWISE NO						
Parame te r			Limit	Units		
Drain-Source Voltage	V _{DS}	20	V			
Gate-Source Voltage			8	v		
	$T_A=25^{\circ}C$	T	33	А		
Continuous Drain Current ^a	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	I _D	27			
Pulsed Drain Current ^b	I _{DM}	±50				
Continuous Source Current (Diode Conduct	Is	13	А			
	$T_A=25^{\circ}C$	PD	16	w		
Power Dissipation ^a	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	гD	10	vv		
Operating Junction and Storage Temperatur	T _J , T _{stg}	-55 to 150	°C			

FREE

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Maximum	Units		
Maximum Junction-to-Ambient ^a	t <= 10 sec	$R_{\theta JA}$	35	⁰ C/MJ		
	Steady State	$R_{\theta JC}$	8	°C/W		

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

b. Pulse width limited by maximum junction temperature

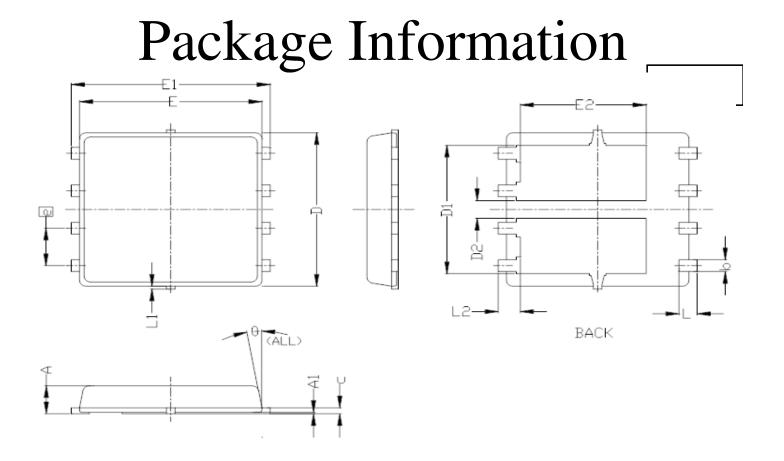
		S OTHERWISE NOTE	Limits				
Parame te r	Symbol	Test Conditions	Min	Тур	Max		
Static							
Gate-Threshold Voltage	V _{GS(th)}	VGS = VDS, ID = 250 uA	1			V	
Gate-Body Leakage	I _{GSS}	$V_{GS} = 8 V, V_{DS} = 0 V$			±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA	
On-State Drain Current ^A	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	20			А	
Drain-Source On-Resistance ^A	r _{DS(on)}	VGS = 4.5 V, ID = 1 A			10	mΩ	
Drain-Source On-Resistance		VGS = 2.5 V, ID = 1 A			13	11122	
Forward Tranconductance ^A	g _{fs}	$V_{DS} = 15 \text{ V}, I_D = 1 \text{ A}$		40		S	
Dynamic					-	<u>-</u>	
Total Gate Charge	Qg	N-Channel		30		nC	
Gate-Source Charge	Q _{gs}	$V_{DS}=15V, V_{GS}=4.5V, I_D=1A$		5			
Gate-Drain Charge	Q_{gd}	•DS=15 •, •GS=+.5 •, 1D=174		10			
Input Capacitance	C _{iss}	N-Channel		3000		pF	
Output Capacitance	C _{oss}	V_{DS} =15V, V_{GS} =0V, f=1MHz		300			
Reverse Transfer Capacitance	C _{rss}	$v_{DS} = 15 v, v_{GS} = 0 v, 1 = 1101112$		200			
Turn-On Delay Time	t _{d(on)}	N-Chaneel		10			
Rise Time	t _r	$V_{DD}=15V, VGS=10V, ID=1A$,		20		nS	
Turn-Off Delay Time	t _{d(off)}	v_{DD} =15 V, VOS=10 V, ID=1A , R _{GEN} =25 Ω		60			
Fall-Time	t _f	$\mathbf{N}_{\text{GEN}} - 2.322$		10		1	

Notes

- a. Pulse test: $PW \le 300$ us duty cycle $\le 2\%$.
- b. Guaranteed by design, not subject to production testing.

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SYMBOLS	DIMENS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES			
STMBOLS	MIN	NOM	MAX	MIN	NOM	MAX		
Α	0.85	0.95	1.00	0.033	0.037	0.039		
A1	0.00		0.05	0.000		0.002		
b	0.30	0.40	0.50	0.012	0.016	0.020		
c	0.15	0.20	0.25	0.006	0.008	0.010		
D	5. 20 BSC			0.205 BSC				
D1	4.35 BSC			0.171 BSC				
D2	0.50	0.60	0.75	0.020	0.024	0.030		
E	5.55 BSC			0.219 BSC				
E1	6.05 BSC			0.238 BSC				
E2	3.82 BSC			0.150 BSC				
e	1.27 BSC			0.050 BSC				
L	0.45	0.55	0.65	0.018	0.022	0.026		
L1	0		0.15	0		0.006		
L2		0.68 REF		0.027 REF				
θ	0°		10°	0°		10°		

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